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3 L30

<u>L29</u>	I12 and I15	13	<u>L29</u>
<u>L28</u>	I12 and I14	60	<u>L28</u>
<i>DB=PGPB; PLUR=YES; OP=OR</i>			
<u>L27</u>	I12 and I14	6	<u>L27</u>
<u>L26</u>	(interrupt\$4 and flag\$4 and application near4 specific and rewrit\$5 near2 register\$1 and exception\$1).clm.	1	<u>L26</u>
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<u>L25</u>	I9 and I17	24	<u>L25</u>
<u>L24</u>	I9 and I16	0	<u>L24</u>
<u>L23</u>	I9 and I15	7	<u>L23</u>
<u>L22</u>	I9 and I14	11	<u>L22</u>
<u>L21</u>	I7 and I17	32	<u>L21</u>
<u>L20</u>	I7 and I16	13	<u>L20</u>
<u>L19</u>	I7 and I15	22	<u>L19</u>
<u>L18</u>	I7 and I14	81	<u>L18</u>
<u>L17</u>	(710/260-266)![CCLS]	1948	<u>L17</u>
<u>L16</u>	(713/401-601)[CCLS]	4206	<u>L16</u>
<u>L15</u>	(712/36,227,244,248)[CCLS]	1766	<u>L15</u>
<u>L14</u>	(712/2-300)[CCLS]	14119	<u>L14</u>
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<u>L13</u>	L11 and I9	37	<u>L13</u>
<u>L12</u>	L11 and I7	195	<u>L12</u>
<u>L11</u>	(inhibit\$4 or hold\$3 or halt\$3 or held or suspend\$3 or suspension\$1) near8 (interrupt\$1 or exception\$1)	21838	<u>L11</u>
<u>L10</u>	L9 and I3	2	<u>L10</u>
<u>L9</u>	I6 and (application near3 specific or asic)	110	<u>L9</u>
<u>L8</u>	L7 and I3	21	<u>L8</u>
<u>L7</u>	L6 and I4	397	<u>L7</u>
<u>L6</u>	L5 and (nested or multip\$7 or plur\$7 or dual or second) near10 (exception\$1 or interrupt\$4)	1688	<u>L6</u>
<u>L5</u>	(exception\$1 or Interrupt\$5) near15 (indicat\$4 or notic\$3 or notif\$7 or detect\$4) near45 return\$5	4365	<u>L5</u>

<u>L4</u>	(delay\$1 or wait\$1) near8 (cycle\$1 or clock\$1 or period\$1)	190439	<u>L4</u>
<u>L3</u>	(rewrit\$4 or rewritable) near5 register	3725	<u>L3</u>
<u>L2</u>	L1 and (nested or multip\$7 or plur\$7 or dual or second) near10 interrupt\$4	1406	<u>L2</u>
<u>L1</u>	Interrupt\$5 near15 (indicat\$4 or notic\$3 or notif\$7 or detect\$4) near45 return\$5	3592	<u>L1</u>

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